

FIG. 1

P2	P1	P0	M	D7	D6	D5	D4	D3	D2	D1	D0
0	0	1	0	R0	*	*	*	*	*	*	*
			1	*	*	*	*	*	*	*	R0
0	1	0	0	R1	R0	*	*	*	*	*	*
			1	*	*	*	*	*	*	R1	R0
0	1	1	0	R2	R1	R0	*	*	*	*	*
			1	*	*	*	*	*	R2	R1	R0
1	0	0	0	R3	R2	R1	R0	*	*	*	*
			1	*	*	*	*	R3	R2	R1	R0
1	0	1	0	R4	R3	R2	R1	R0	*	*	*
			1	*	*	*	R4	R3	R2	R1	R0
1	1	0	0	R5	R4	R3	R2	R1	R0	*	*
			1	*	*	R5	R4	R3	R2	R1	R0
1	1	1	0	R6	R5	R4	R3	R2	R1	R0	*
			1	*	R6	R5	R4	R3	R2	R1	R0
0	0	0	0	R7	R6	R5	R4	R3	R2	R1	R0
			1	R7	R6	R5	R4	R3	R2	R1	R0

FIG. 2

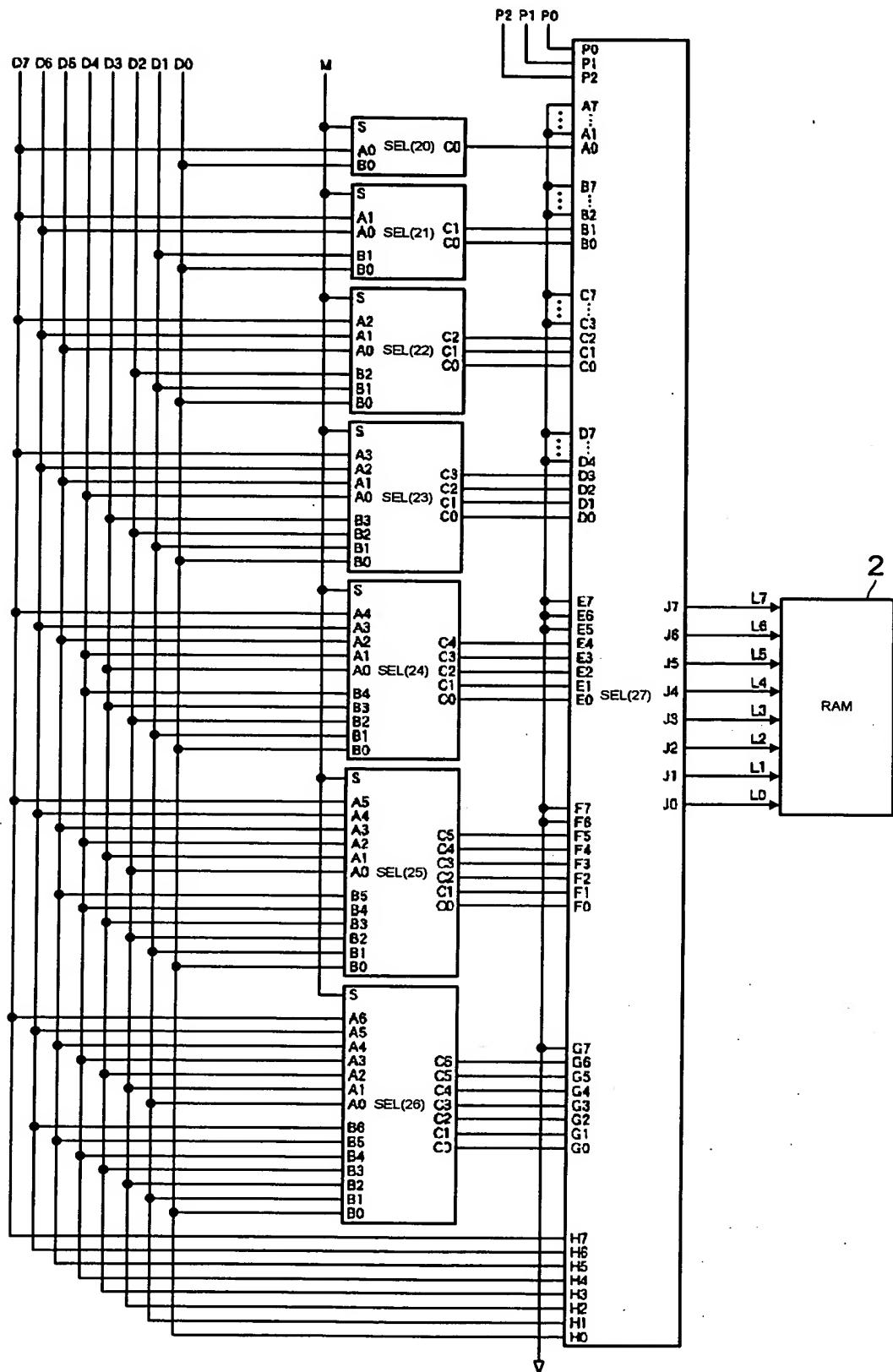


FIG. 3

lines of a data bus	D7	D6	D5	D4	D3	D2	D1	D0
data	R4	R3	R2	R1	R0	*	*	*

FIG. 4